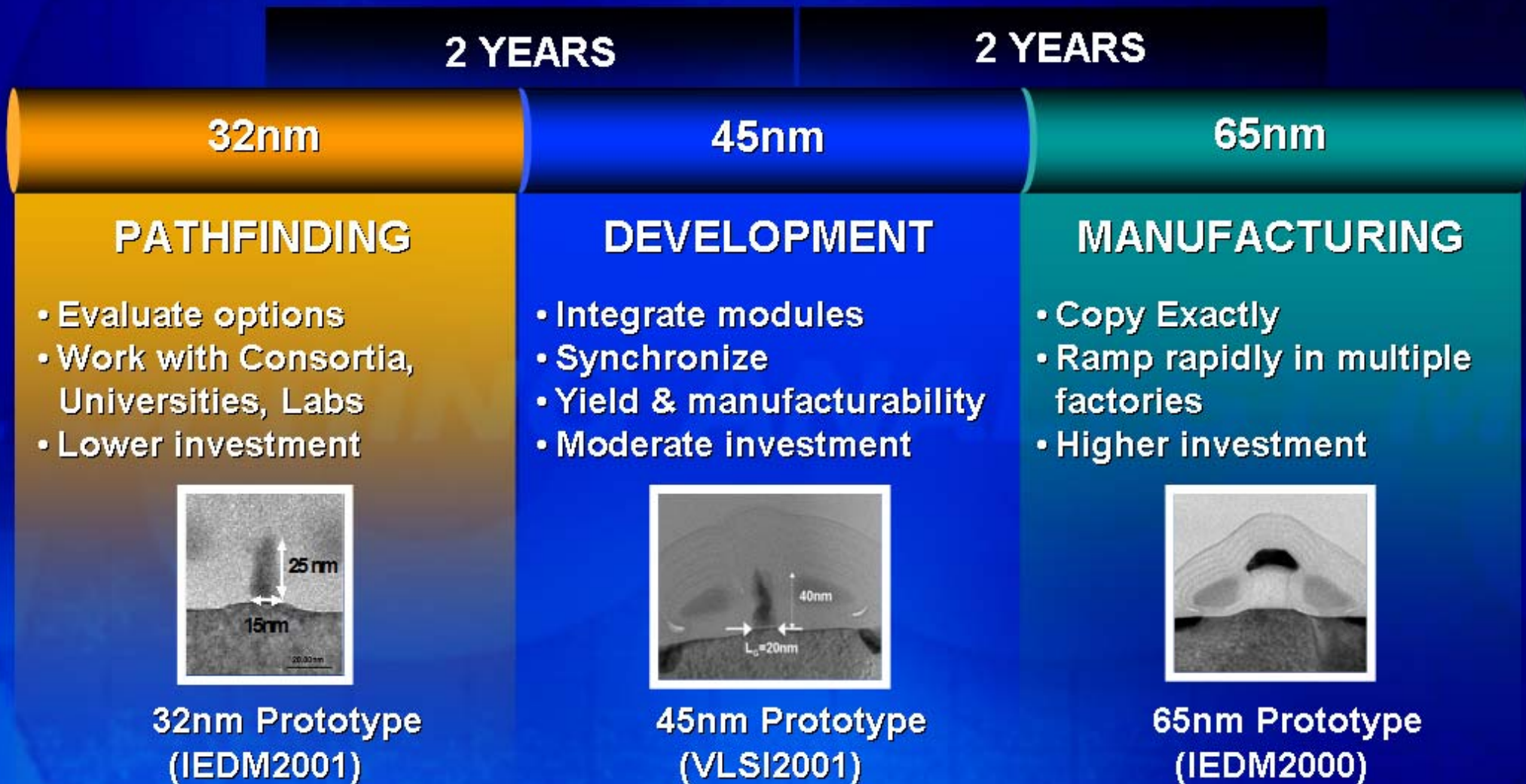
The logo features the year '2005' in large, bold, yellow 3D-style numerals. A circular inset, resembling a microchip with a blue and green grid pattern, is positioned behind the zeros. The words 'INTEL SPRING ANALYST MEETING' are written in white, bold, sans-serif capital letters across the middle of the '2005' numerals. The entire graphic is set against a dark blue background with a subtle, larger-scale grid pattern.

# 2005 INTEL SPRING ANALYST MEETING

**Robert J. Baker**

**Senior Vice President, General Manager  
Technology and Manufacturing Group**

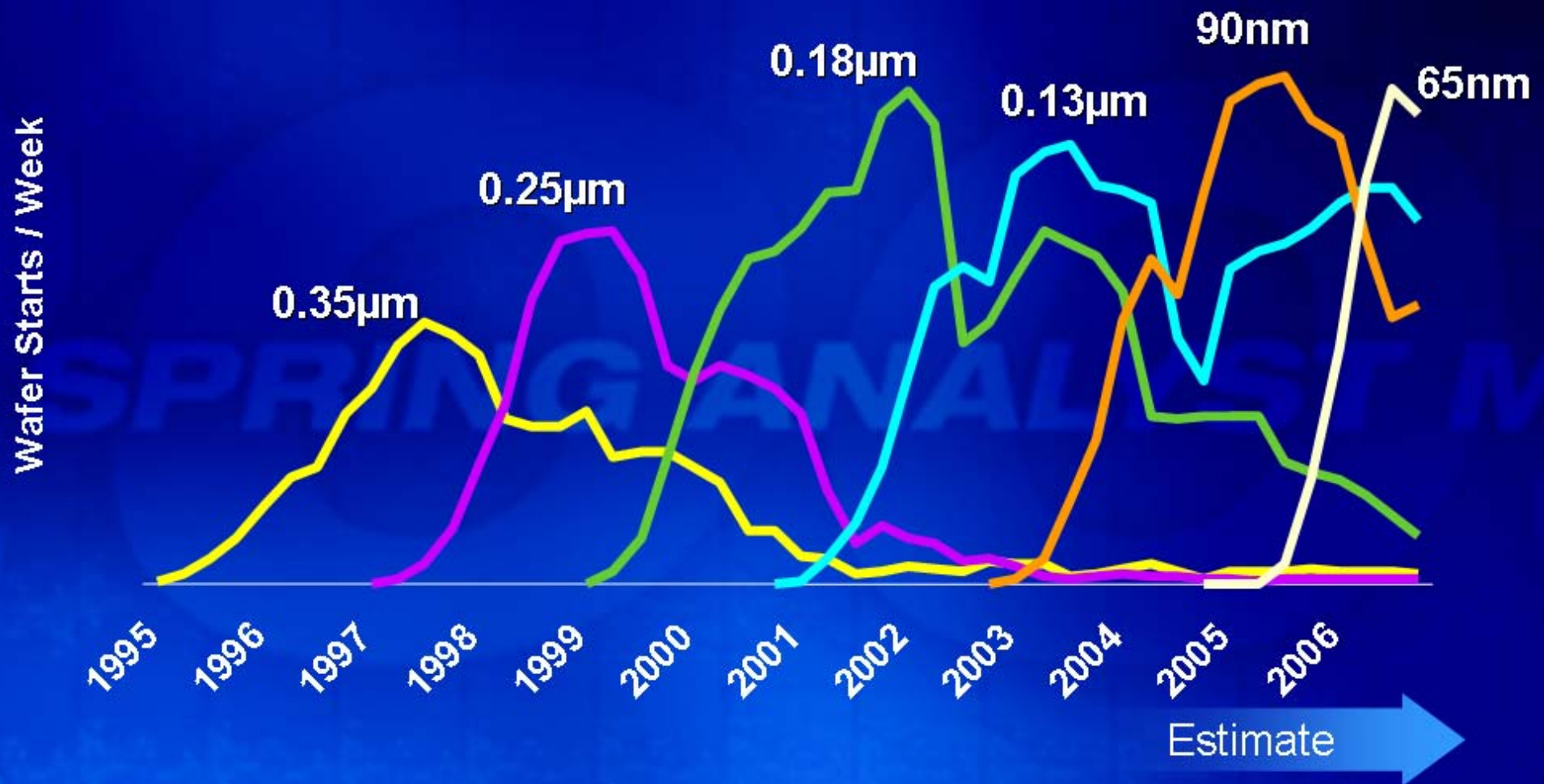
# Intel's Technology & Manufacturing Pipeline



**Unique Advantage: Innovation Delivered**



# Intel's Technology & Manufacturing Pipeline

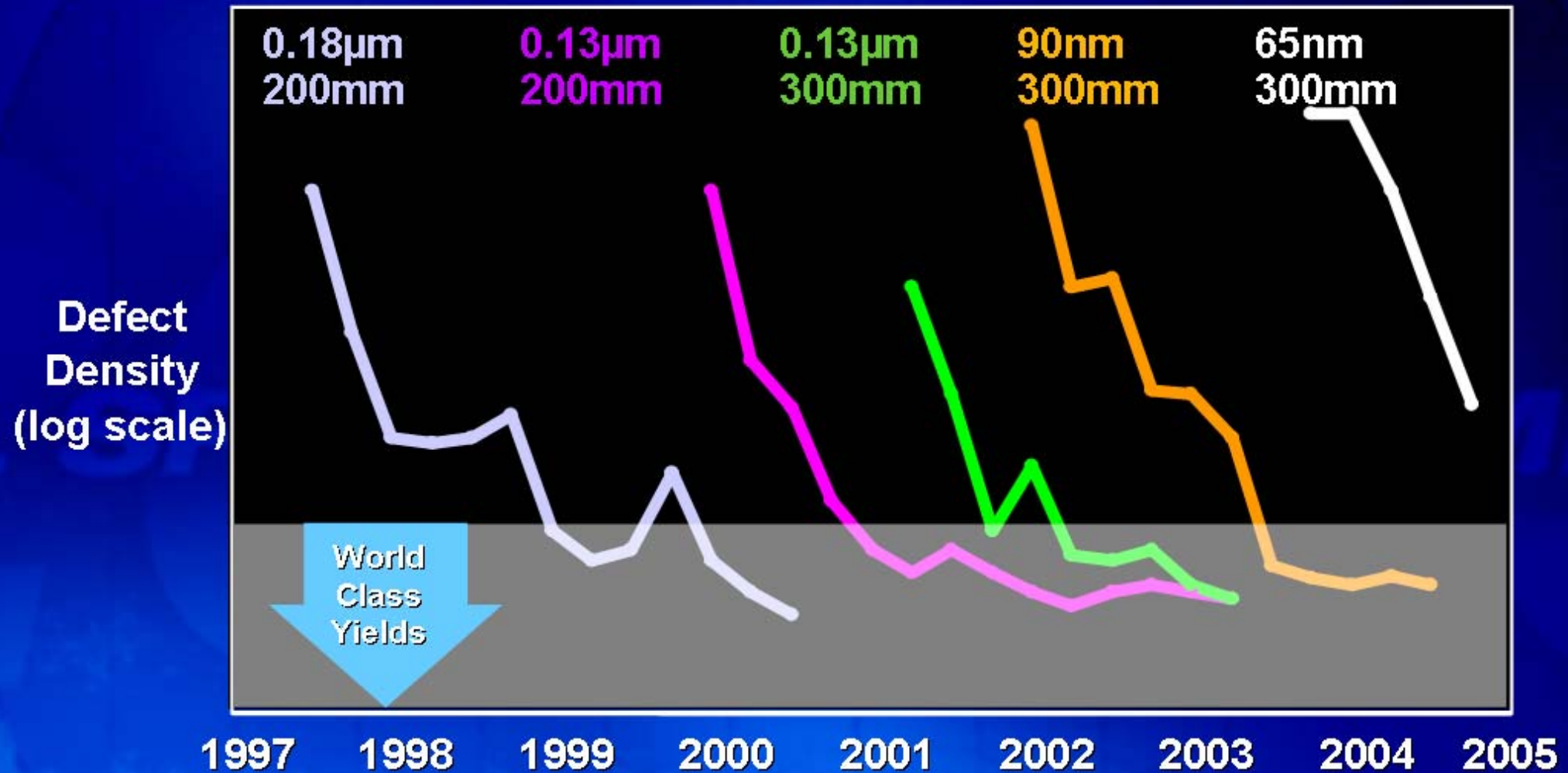


**A Unique Advantage: Faster Ramps**



Source: Intel

# Intel's Technology & Manufacturing Pipeline



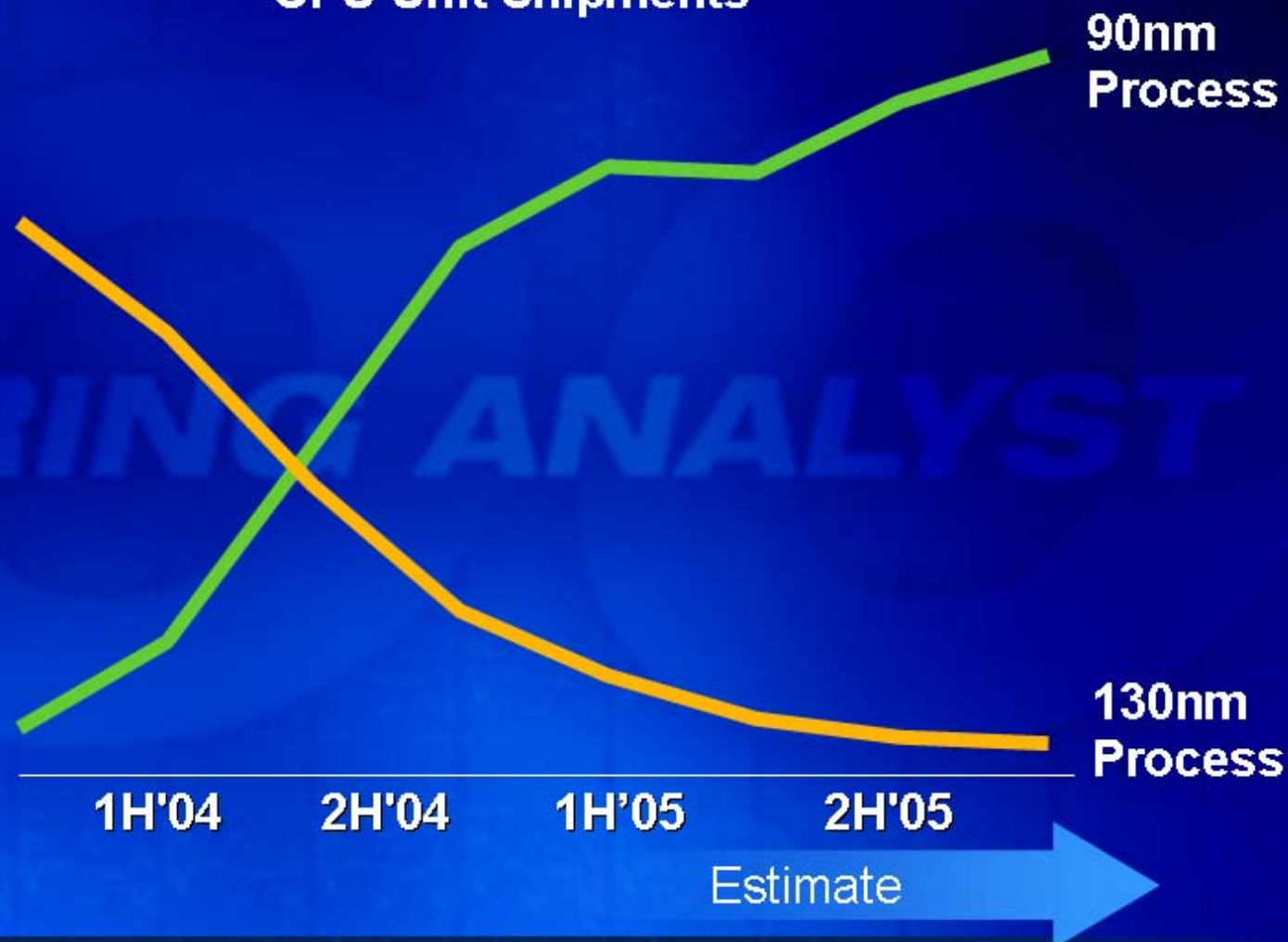
**A Unique Advantage: Higher Yields**





# 90nm Technology Ramp Ahead of Industry

CPU Unit Shipments



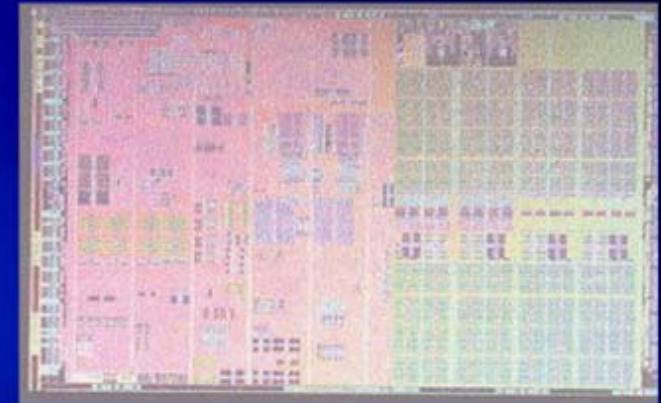
**300mm Production at High Volume**



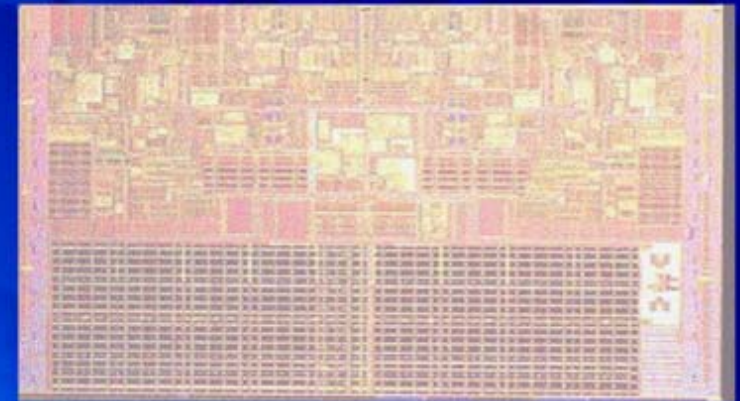
# Intel Sets Pace for 65nm

- Process and product development goals demonstrated
  - 2<sup>nd</sup> Generation Strained Silicon
  - 35nm Gate Length
- Yield trend on track
  - Module characterization
- Yonah shipping in 2<sup>nd</sup> half of 2005

Cedarmill\*



Yonah\*

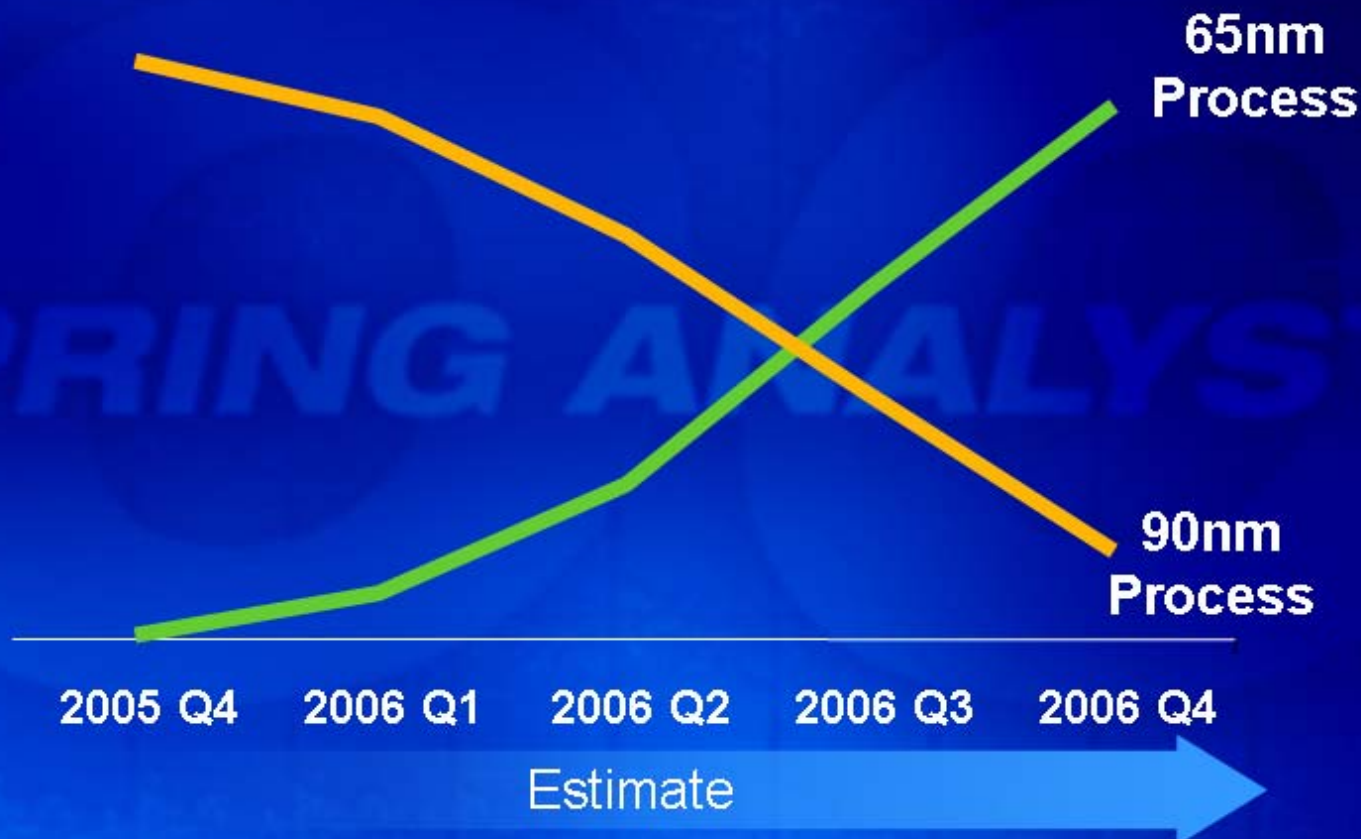


\* Die not to scale



# Leading Edge 65nm Capacity

Total CPU Projected Shipments



**90nm to 65nm Crossover Projected for Q3'06**

# 300mm Fab Manufacturing



**The Scale to Deliver Platforms**

Source: Intel



# The Cost Advantage: Capital Reuse

The Long and Productive Life of Fab 12

## Timeline



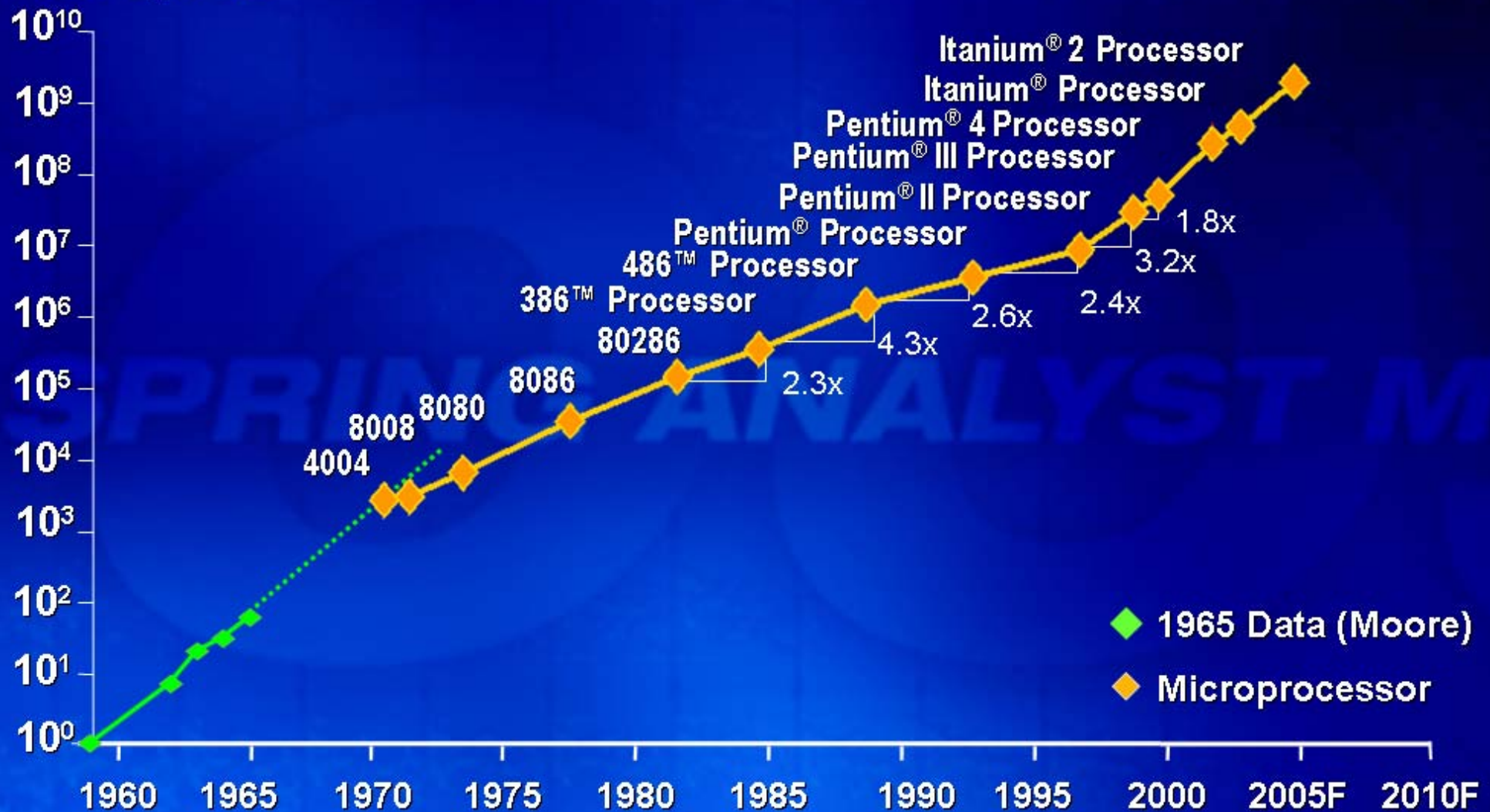
## Facility Investments (\$)



**Maximizing ROI on Invested Capital**

# 40<sup>th</sup> Anniversary of Moore's Law

Transistors per Die



**Innovation Continues to Drive Silicon Scaling**

Source: Intel internal



# Summary

- Intel's technology and manufacturing pipeline = a unique advantage
- 90nm technology ramp is ahead of the industry
- 65nm process on track for production in 2005
- Manufacturing scale: high volume low cost manufacturing
- Moore's Law: density, performance, cost